

Design of Remote Laboratory dedicated to E2LP board for e-learning courses.

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Abstract—Recently, with advent and exploitation of computer and communication technologies, remote laboratories have been widely popular among many universities. They are built in order to enhance learning and minimize the gap between theory and practice. Remote laboratories provide on-line pervasive workbenches, which allow an interactive learning environment that maintains student attention. This paper reports on development of remote laboratory, which is currently performing under E2LP FP7 project. The paper addresses many solutions in the development stages along with powerful technologies involved. E-learning portal will create and provide new teaching methods in embedded systems design through the real and simulated experiments.

I. INTRODUCTION

Laboratories, which are found in all engineering and science programs, are an essential part of the education experience. Not only do laboratories demonstrate course concepts and ideas, but they also bring the course theory into alive. In a traditional laboratory, the user interacts directly with the equipment by performing physical actions (e.g. manipulating with the hands, pressing buttons, turning knobs) and receiving sensory feedback (visual and audio). However, equipping a laboratory is a major expense and its maintenance can be difficult. [1]

Since the experiments are performed in a laboratory that contains expensive equipment, the students must be supervised which limits the time they have. This also requires a class with many groups performing the experiment at the same time, and thus many instruments are required to support each group. Laboratory experiments are also a serious problem for distance learning students who may not have an access to the laboratory at all. [2]

As an alternative, virtual online laboratories can expose students to hands-on learning without incurring the high costs of instructional facilities. [3]

Remote laboratories are those laboratories that can be controlled and administrated online. They differ from the virtual simulated laboratories as they are interacting with physical instruments. [4]

II. REMOTE LABORATORY

In E2LP (Embedded Computer Engineering Learning Platform) project [5] a Remote Laboratory (RL) is an experiment, demonstration and a process running locally to design and control an experiment board based on a FPGA device, but with the ability to be monitored and controlled over the Internet (future E-learning portal).

In the base case, the RL can be an experiment board connected to a computer through a standard interface and with the host computer connected to the Internet, which provide remote access. The client can be any computer connected to the Internet with an ability to see the same interface as the local host and also have the same programs, interfaces, modules etc.

The concept of E2LP RL should allow the user to do several actions over an Internet connection, which are the list of E2LP Remote Laboratory functionalities:

1. Dedicated software and hardware solutions will provide an access to laboratory equipment and enable students to set them up and operate them at the required level to carry out exercises.

2. Users could access the essential data sheets, tutorials and software tools, which are available on the E-learning portal as an introduction to the course, which is a RL's content management system (CMS). Each laboratory exercise is presented to the user through tabs and such division will be implemented into Moodle based platform for e-learning course (Basic information, Theoretical explanations Instructions, Feedback questionnaire for lab evaluation).

3. After booking in a given time space Users could remotely program given set of exercises over the Internet and simultaneously, in real time could monitor the evolution of the experiment on implemented dedicated Graphical User interface (GUI) of the Front Panel of the board. Fig. 1

4. Automatic verification of course assignments, supported by Moodle plug-in, will allow an advanced management of assignments and submissions together with feedback information mechanisms for both teachers and stu-

dents, which will verify whether the students designs work or not according to the specifications.

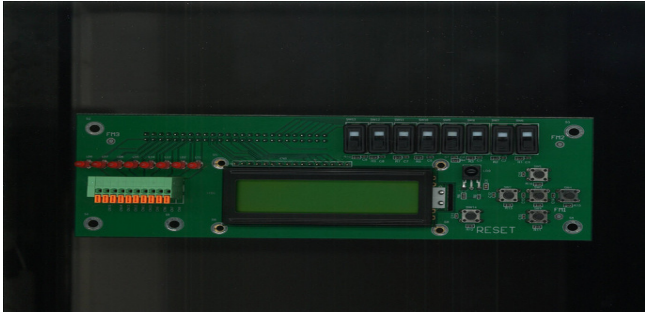


Fig. 1 E2LP board Front Panel

The main advantage of E2LP Remote Laboratory is when students interact with the dedicated boards UI, implemented as a web service and work with software applications, they are actually operating the same tools and instruments like they would be in classic lesson in laboratory.

RL is a gate which provide an access to continuously refreshed interfaces and signals from the real board and enable user remotely control and program the board directly from their computer at home, having instant visual feedback.

To achieve this, it is necessary to forward data directly to the server over common interfaces or over local network by using dedicated hardware solutions and specified proper router configuration.

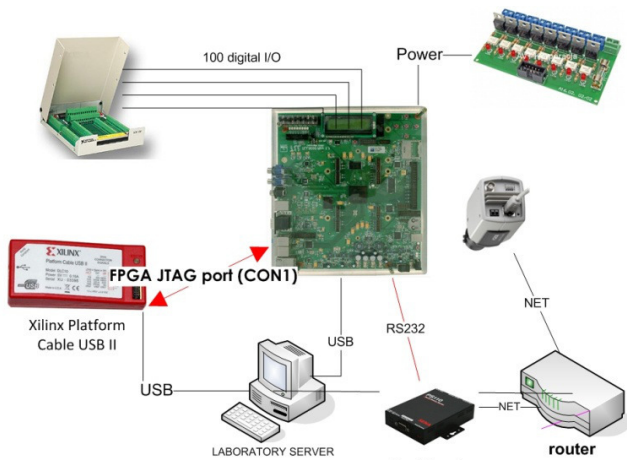


Fig. 2 Remote Laboratory concept of solution

The Fig. 2 above presents remotely controlled environment concept of solution. The whole environment is managed by powerful E2LP Server, which is equipped with all common interfaces, which are essential for internal hardware and software compatibility. E2LP Server is connected via Ethernet interface to the local network, which is responsible for seamless data communication between environment's components. The crucial component of the remotely

controlled environment is an experiment base board, which is controlled by programming device (Xilinx Platform HW-USB-II-G). This programming device provides integrated firmware to deliver high-performance, reliable and user-friendly configuration of the base board and enables user to program other Xilinx CPLD devices. This programming device is fully integrated and optimized for use with specialized Xilinx iMPACT software, which enable users to perform remote operations such as programming and configuring FPGA via JTAG interface.

The NI PCI-6509 digital card with 96 bidirectional I/O lines enable user by dedicated GUI interface (Error: Reference source not found) to control each pin in the boards front panel interface and consequently enable him to control each led, switch and button. Furthermore specific module communicates with LCD pins on boards front panel interface and translate them into RS232 ASCII chars.

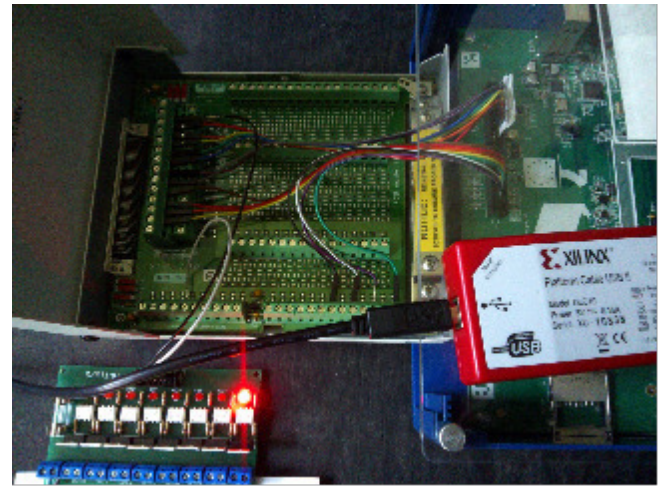


Fig. 3 NI PCI-6509 digital card integration with E2LP board front Panel

In remote operations user firstly power the board from the website (this is done by power controller module and digital card) and run iMPACT on one computer but the operations are performed on a device attached to another computer through a Xilinx Cable Server. Xilinx ISE includes such program as well as provide a set of programming tools, which allow user to perform operations remotely. To use this functionality user only needs to specify a remote server address in proper configuration in iMPACT software. This is the most important feature of programming device, from the RL point of view.

It should be pointed that all exercise could be done remotely, but feedback from some interfaces is not available. Error: Reference source not found below represents implemented necessary interfaces according to requirements.

Connection with the Remote Laboratory is provided via e-learning portal and is based on Apache server, PHP and SQL server. It provides an access to knowledge (exercises, data sheets) through a web user interface and has an ability

TABLE I
IMPLEMENTED INTERFACES IN RL

Component		Implementation in RL	Access to the interface
Type	Direction (In/Out)		
LCD 16x2 Character Display	f/📡	(Not-tested) Full ¹	GUI
Dip Switches	📡/f	Full	GUI
Push Buttons	📡/f	Full	GUI
LED	f/📡	Full	GUI
RS-232	📡/📡	Full	Standard tool ²
Power Supply ON/OFF	📡/f	Full	GUI

¹ – now under development
² – e.g. HyperTerminal, Putty, etc.
 GUI – web interface

to exchange information between laboratory hardware and software applications. The second role of e-learning portal is management of users, which enables them access to the laboratory hardware and software (booking functionality and authorization). In E2LP project the e-learning platform is based on Moodle Platform, which is one of the most popular open source learning management systems.

III. SCENARIO OF USAGE RL

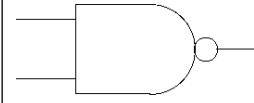
For remote operations user is provided user manual guideline, dedicated software and an access to laboratory equipment, which enable students to set them up and carry out exercises. Our aim was to provide instant feedback to the board in a way that user would work with the real board as if it was connected locally. This functionality was a purpose to developed GUI web interface of front panel that exactly looks like the real board, which has connections to real signals from the real board. States of the physical signals are continuously refreshed at defined times (250 ms) and tests confirmed that it is sufficient to inform the user (by observing GUI) that his program – made during the lab exercise – works correctly. Below we described current scenario of RL usage during the exercise of design the logic circuits:

- User write code of his program according to exercise on local Xilinx ISE environment
- Generate bit file
- Remotely configure FPGA (setup the board, power up, connect the programming modules,)

- Test solution using GUI and standard tool

In the design of digital systems logic circuits are used as fundamental components. TABLE II presents the NAND (Negated AND or NOT AND) logic table its truth table inherited from Boolean algebra. After setting the input voltages to the desired values (low or high), these circuits are capable of calculating values of Boolean functions they represent and present the results of Boolean functions on their outputs. and this exercise will not go into detail on how these circuits are implemented.

TABLE II
NAND LOGIC TABLE

$Y = X_1'X_2$ 	X_1	X_2	Y
	0	0	1
	0	1	1
	1	0	1
	1	1	0

System entity represents the system as seen from outside. Imagine observing the system on Fig. 4 from outside, without the possibility to see what is inside the system. The only things you will see are its input and output ports. Input ports are like input variables to a function, while output ports are like results of the functions. Each output port in a combinational digital system represents the result of one Boolean function of input ports. If the system has N output ports and M input ports, it computes N Boolean functions of M variables, i.e. functions of the same input ports.

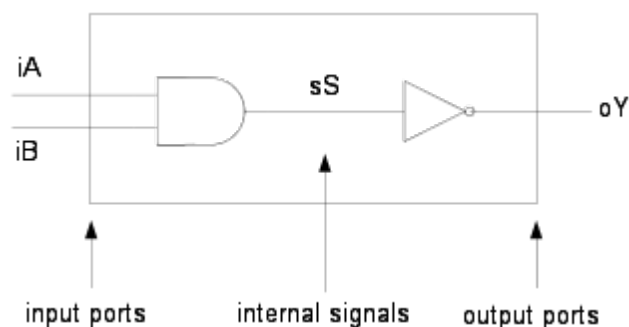


Fig. 4 An example of the digital system

In order for the tool to know to which components on the board we want to connect inputs and outputs of our system, we need to specify which pin from FPGA we want to associate with which port of our system. Let us connect inputs iA and iB to two switches and output oY to a LED. You can always refer to the complete list of FPGA pins on E2LP platform and to which components they are connected. Pin assignment is done in a special tool for that, the Xilinx PlanAhead. It can be run from Xilinx ISE.

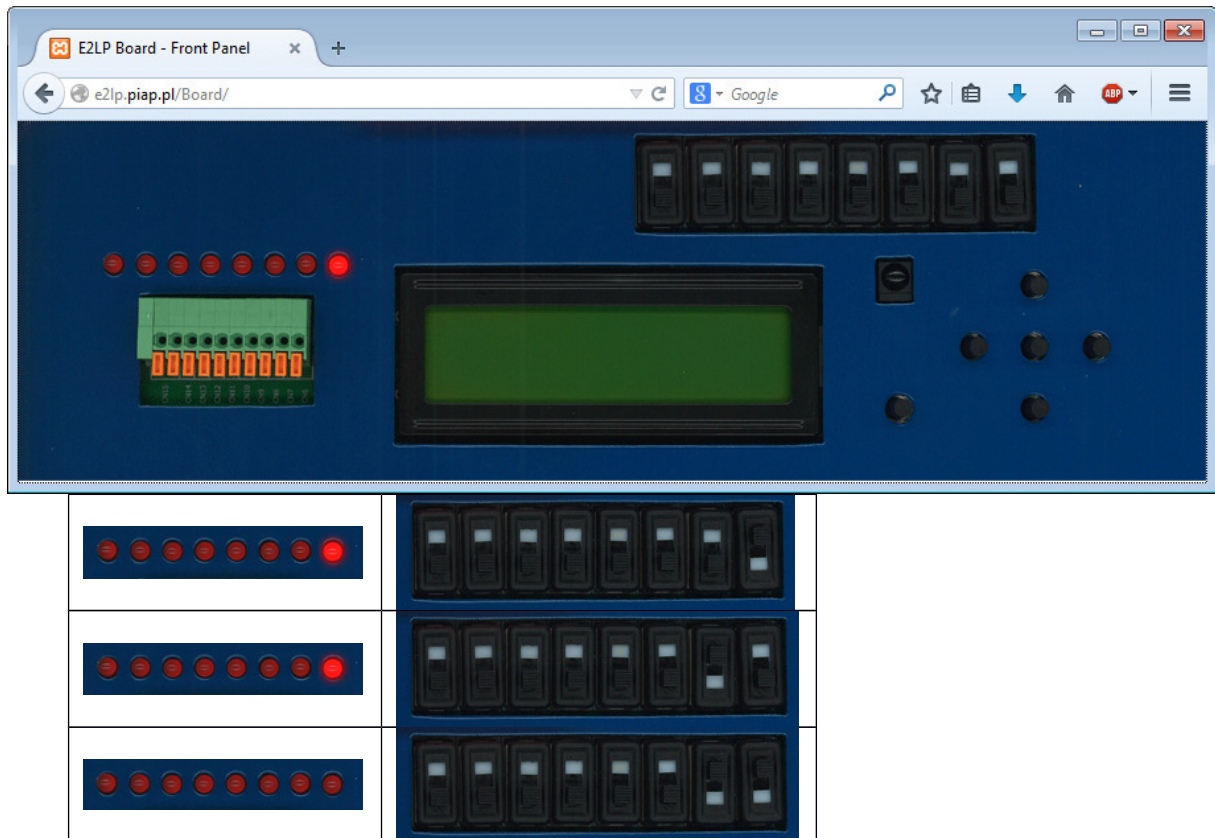


Fig. 5 Evolution of the exercise on GUI web interface

If configuration completes without errors, the FPGA_DONE diode on the board should be turned on, meaning that the FPGA is configured and working. If you change the state of the switches connected to inputs iA and iB , you should observe the corresponding change to the LED connected to output oY based on a Boolean function which the circuit implements (NAND). Fig. 5

IV. CONCLUSIONS

Proposed solutions based on integrated together Remote Laboratory components and e-learning Moodle Platform enable student to acquire desired knowledge about digital systems and significantly support learning process.

Remote laboratory and e-learning portal enable user to access E2LP base board over the Internet, configure it compiling VHDL code and having feedback immediately on his own computer. In the same time he can monitor E2LP base board and operate on remote laboratories equipment.

LISTING: UCF CONSTRAIN FILE DEFINITION OF THE DIGITAL SYSTEM FROM FIGURE 4

```
NET "iB" LOC = Y24;
NET "oY" LOC = N24;
NET "iA" LOC = W19;
```

LISTING: VHDL DESCRIPTION OF THE NAND FUNCTION IMPLEMENTATION DIGITAL SYSTEM FROM FIGURE 4

```
library IEEE;
use IEEE.STD_LOGIC_1164.ALL;

entity MyFirstDigitalSystem is
    Port ( iA : in  STD_LOGIC;
          iB : in  STD_LOGIC;
          oY : out STD_LOGIC);
end MyFirstDigitalSystem;

architecture Behavioral of
MyFirstDigitalSystem is

    signal sS : std_logic

begin

    sS <= iA and iB;
    oY <= not(sS);

end Behavioral;
```

Future development of RL based on integration of external equipment and services and development of Moodle platform will be done in accordance with the exercises necessities and end-users needs. .

V. REFERENCES

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- [5] <http://www.e2lp.org/>